



Attorney Docket No. 040373-0300

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Toshiyuki HIROTA et al.

Group Art Unit: 2811

Serial No. 09/741,195

Examiner: C. Q. Nguyen

Filed: December 21, 2000

For: SEMICONDUCTOR DEVICE WITH HIGH- AND LOW-DENSITY REGIONS
OF TRANSISTOR ELEMENTS ON SINGLE SEMICONDUCTOR SUBSTRATE,
AND METHOD OF MANUFACTURING SUCH SEMICONDUCTOR DEVICE

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated December 3, 2001, applicants hereby elect the claims of Group II, claims 1-4, 6-9, 11-16, and 18-23, drawn to a method of making a semiconductor device, for prosecution in the subject application.

Applicants, of course, reserve the right to file a divisional application covering the subject matter of the non-elected claims.

Receipt of the initial Office Action on the merits is awaited.

Respectfully submitted,

January 3, 2002
Date

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